



2k bit Read/Write High Secure ISO15693 Standard Compliant Device

General Description

The EM4233 is a long range passive CMOS integrated circuit intended for use in applications requiring a contactless read/write memory offering a high and tailored level of security.

The configurable 2k bit EEPROM memory included in the chip is organized in 64 words of 32 bits. The high level of flexibility in the memory management, permits that a memory word can be read/write protected and/or locked separately.

This latest generation of EEPROM memory offers data retention of 60 years enabling solutions for long-term asset management applications.

The customer data privacy and security is guaranteed by a powerful and fast crypto engine implemented in the chip associated with a true random generator and a 96 bit secret key.

The enhanced on-chip security feature permits a flexible administration of the memory access rights which makes it the right solution for advanced theft protection.

Depending on the application requirements, in terms of security, the user can tailor and adjust the security level by selecting either a true mutual authentication process, a login procedure with a 32 bit password or use the chip as a plain text memory.

The IC supports all the ISO/IEC 15693 Mandatory commands and many of the optional commands. Its command set is completed by unique custom commands which give to the EM4233 customers a higher degree of differentiation in terms of security, flexibility and data protection.

Each EM4233 contains in its memory a 64 bit unique serial number which cannot be altered and guarantees the uniqueness of each device.

Block Diagram

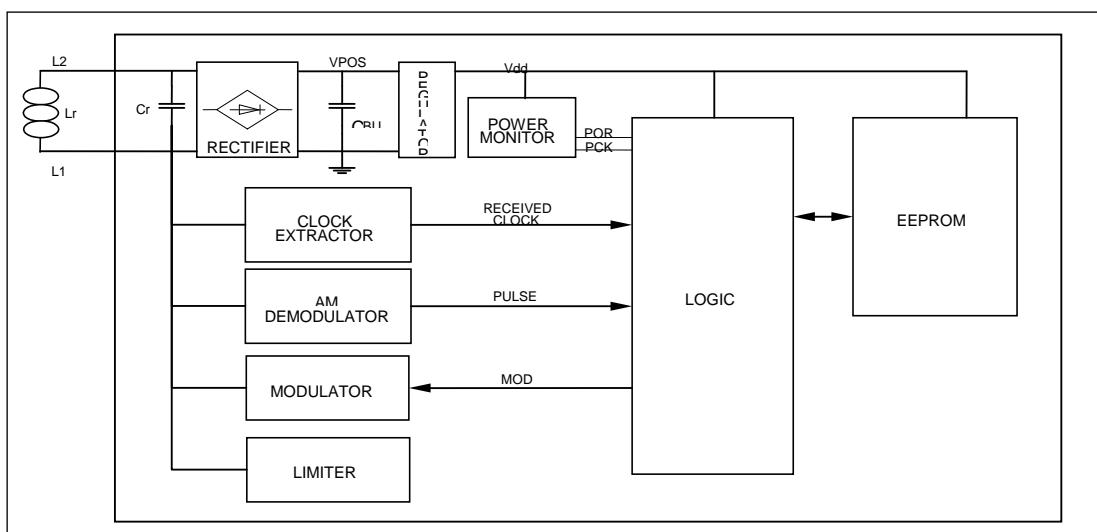


Figure 1